

SYSTEM AND METHOD FOR TESTING A DEVICE

ABSTRACT OF THE DISCLOSURE

A system for testing a device includes a processor that operates to execute instructions, where the instructions are used to test a device. The processor 5 also operates to generate test signals associated with the test instructions. An interface apparatus is coupled to the processor and operates to communicate the test signals to the device. The interface apparatus includes connectors, where each connector operates to communicate 10 at least one of the test signals.